## **IN THE SPECIFICATION**

Please insert the following paragraph on page 1 after the title of the invention and before the "Technical Field":

## -- Related Application

This application is a national phase of PCT/JP2005/002159 filed on February 14, 2005, which claims priority from Japanese Application No. 2004-122075 filed on April 16, 2004, the disclosures of which Applications are incorporated by reference herein. The benefit of the filing and priority dates of the International and Japanese Applications is respectfully requested.--

The following paragraph will replace all prior versions of itself in the specification of the application.

On page 6, lines 15-16, please amend the paragraph as follows:

Specifically, a reference voltage generation circuit of the present invention is a reference voltage generation circuit for generating a constant reference voltage at a reference voltage output terminal, comprising: a first diode element having a cathode connected to a ground potential; a second diode element which has a current density different from that of the first diode element and whose cathode is connected to the ground potential; a first resistive element having an end connected to an anode of the second diode element; a second resistive element having an end connected to the other end of the first resistive element, the other end of the second resistive element being connected to the reference voltage output terminal; a third resistive element having an end connected to the anode of the first diode element and the other end connected to the reference voltage output terminal; a first P-type transistor for supplying a

current to the reference voltage output terminal; a second P-type transistor having a gate terminal connected to its own drain terminal and to a gate terminal of the first P-type transistor; a bandgap reference circuit having a feedback type control circuit for controlling a drain current of the second P-type transistor such that a voltage at the anode of the first diode element is equal to a voltage at a connection point between the first and second resistive elements; and a start-up circuit for, if an output voltage of the reference voltage output terminal of the bandgap reference circuit is at an abnormal stabilization point, shifting the output voltage from the abnormal stabilization point to a normal stabilization point, wherein the start-up circuit is provided between the drain terminal of the second P-type transistor of the bandgap reference circuit and the ground potential and, a current consumed by the start-up circuit is supplied from the drain terminal of the second P-type transistor, and if the drain current of the second P-type transistor is substantially zero, the start-up circuit increases the drain current of the second P-type transistor.